# EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L4 | 105135 | (SIMM$1 SDRAM$1 DRAM$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | ON | 2006/02/16 08:22 |
| L5 | 7901 | L4 with (memory$2 near2 control$5) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | ON | 2006/02/16 08:23 |
| L6 | 144 | L5 same (PCB$1 (print$2 near2 board$2)) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | ON | 2006/02/16 08:23 |
| L7 | 0 | ("2001/0048618").URPN. | USPAT | OR | ON | 2006/02/16 08:31 |
| L8 | 5931 | (memory$2 adj controller$2) near4 (mount$3 affix$3 connect$3) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | ON | 2006/02/16 09:04 |
| L9 | 30 | L8 with (pcb$1 (print$2 near2 board$3)) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | ON | 2006/02/16 09:13 |
| L15 | 1 | "6349051".pn. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | ON | 2006/02/16 09:13 |
| L16 | 206 | "memory controller" with (pcb "printed circuit board" "printed wiring board") with (memory$2 DRAM$2 SDRAM$2 SRAM$3) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | ON | 2006/02/16 09:58 |
| L17 | 70 | "memory controller" with (pcb "printed circuit board" "printed wiring board") with (memory$2 DRAM$2 SDRAM$2 SRAM$3) with (mount$2 affix$3) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | ON | 2006/02/16 09:59 |